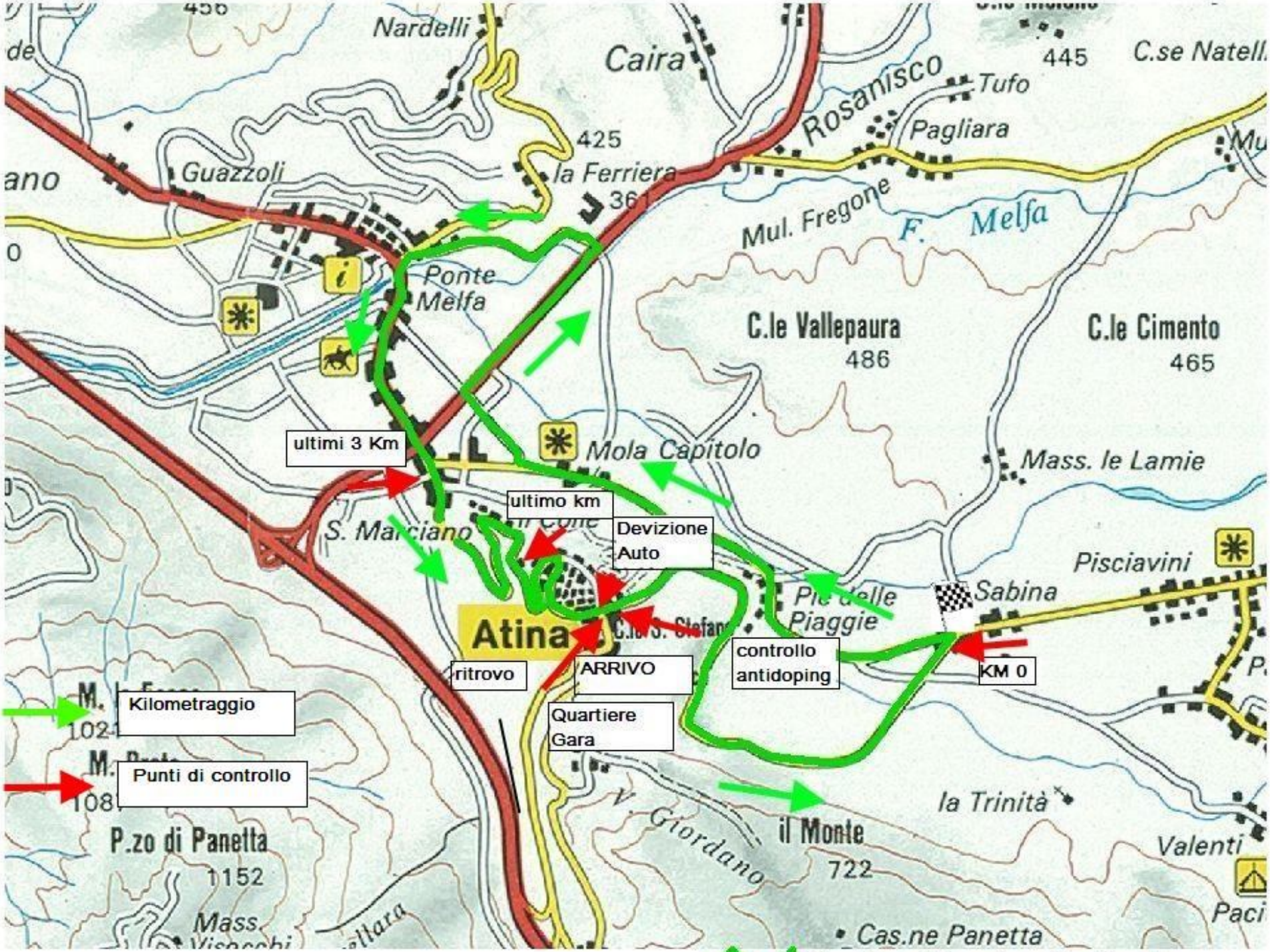

Planimetria circuito finale